App. No. 10/652,627
Office Action Dated January 25, 2006

REMARKS

Favorable reconsideration of this application is requested in view of the above amendments and the following remarks. Claims 1 and 2 are hereby amended.

Amendments of claims 1 and 2 are supported by page 8, lines 14-21.

Claims 1-8 were rejected as being unpatentable over applicant's admitted prior art in view of Yamamoto (US 2002/0140685). Applicant traverses this rejection. The rejection relies on Yamamoto to teach an area image data generating section. However, Yamamoto does not teach an image display system or a display device including an area image data generating section for supplying an area image data which is corresponding to a specific area of a captured image data by a image capturing device to said display memory, where the supplying of the area image data generating section is provided by a DMA data transfer, as required by claims 1 and 2.

Rather, Yamamoto teaches a compare unit (24, the rejection equates to the claimed area image data generating section) that compares one line of a first (previous) screen image stored in a line buffer A (16) with one line of a second (current) screen image stored in a line buffer B (20) to determine if any inconsistencies exist between the previous frame and the current frame. If an inconsistency is determined, the inconsistent line of the screen image data is transmitted to a data transmission unit (8, rejection equates to the claimed display memory). Therefore, Yamamoto teaches supplying a single line of an entire screen image data. Yamamoto does not suggest checking for inconsistencies between lines in a specific area of the screen image, but rather is directed towards the entire captured image (full screen). Further, Yamamoto does not suggest DMA data transfer of data of a specific area of a captured image. In fact, since Yamamoto transfers only one line at a time, a DMA transfer would not be needed. See Figure 1 and paragraphs 51-52.

Claims 1 and 2 provide an image display system and a display device in which only the captured image data of the specific area is DMA transferred thereafter so that the specific area of the captured image is subsequently displayed together with a frame image to form an in-frame

5

App. No. 10/652,627
Office Action Dated January 25, 2006

captured image. Since only the image data of the specific area is transferred, the amount of transferred data can be reduced, thereby increasing control and data bus bandwidth for other operations, as well as reducing processing load on the CPU. The inventions of claims 1 and 2 provide an increase in display frame rate, and provide a smoother moving picture display. See page 2, lines 13-17 and page 9, lines 9-21 of the current application.
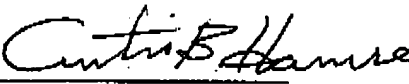
Favorable reconsideration of claims 1-8 is requested.

In view of the above, favorable reconsideration in the form of a notice of allowance is requested. Any questions regarding this communication can be directed to the undersigned attorney, Curtis B. Hamre, Reg. No. 29,165, at (612)455-3802.

Respectfully Submitted,

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6